

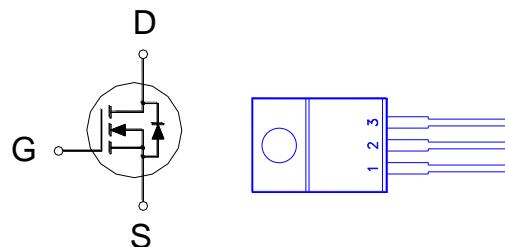
**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****PP4B10AT**

TO-220

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

| $V_{(BR)DSS}$ | $R_{DS(ON)}$ | $I_D$ |
|---------------|--------------|-------|
| 100V          | 4.5mΩ        | 137A  |


 1. GATE  
 2. DRAIN  
 3. SOURCE
**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$  Unless Otherwise Noted)**

| PARAMETERS/TEST CONDITIONS                     |                     | SYMBOL         | LIMITS     |  | UNITS |
|--|---------------------|----------------|------------|--|-------|
| Drain-Source Voltage                           |                     | $V_{DS}$       | 100        |  | V     |
| Gate-Source Voltage                            |                     | $V_{GS}$       | $\pm 20$   |  | V     |
| Continuous Drain Current                       | $T_C = 25^\circ C$  | $I_D$          | 137        |  | A     |
|  | $T_C = 100^\circ C$ |                | 97         |  |       |
| Pulsed Drain Current <sup>1</sup>              |                     | $I_{DM}$       | 350        |  |       |
| Avalanche Current                              |                     | $I_{AS}$       | 28         |  |       |
| Avalanche Energy                               | $L = 1mH$           | $E_{AS}$       | 392        |  | mJ    |
| Power Dissipation                              | $T_C = 25^\circ C$  | $P_D$          | 187        |  | W     |
|  | $T_C = 100^\circ C$ |                | 93         |  |       |
| Operating Junction & Storage Temperature Range |                     | $T_j, T_{stg}$ | -55 to 175 |  | °C    |

**THERMAL RESISTANCE RATINGS**

| THERMAL RESISTANCE  | SYMBOL          | TYPICAL | MAXIMUM | UNITS  |
|---------------------|-----------------|---------|---------|--------|
| Junction-to-Case    | $R_{\theta JC}$ |         | 0.8     |        |
| Junction-to-Ambient | $R_{\theta JA}$ |         | 62.5    | °C / W |

<sup>1</sup>Pulse width limited by maximum junction temperature.
**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ C$ , Unless Otherwise Noted)**

| PARAMETER                       | SYMBOL        | TEST CONDITIONS                                 | LIMITS |     |           | UNITS   |
|---------------------------------|---------------|---|--------|-----|-----------|---------|
|                                 |               |   | MIN    | TYP | MAX       |         |
| <b>STATIC</b>                   |               |   |        |     |           |         |
| Drain-Source Breakdown Voltage  | $V_{(BR)DSS}$ | $V_{GS} = 0V, I_D = 250\mu A$                   | 100    |     |           | V       |
| Gate Threshold Voltage          | $V_{GS(th)}$  | $V_{DS} = V_{GS}, I_D = 250\mu A$               | 2      | 2.5 | 4         |         |
| Gate-Body Leakage               | $I_{GSS}$     | $V_{DS} = 0V, V_{GS} = \pm 20V$                 |        |     | $\pm 100$ | nA      |
| Zero Gate Voltage Drain Current | $I_{DSS}$     | $V_{DS} = 100V, V_{GS} = 0V$                    |        |     | 1         | $\mu A$ |
|                                 |               | $V_{DS} = 100V, V_{GS} = 0V, T_J = 125^\circ C$ |        |     | 100       |         |

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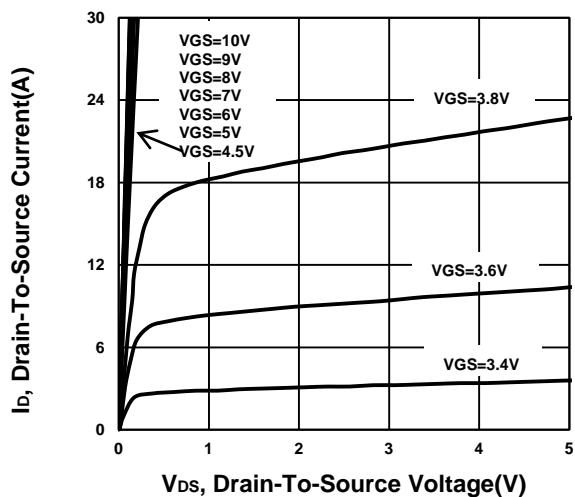
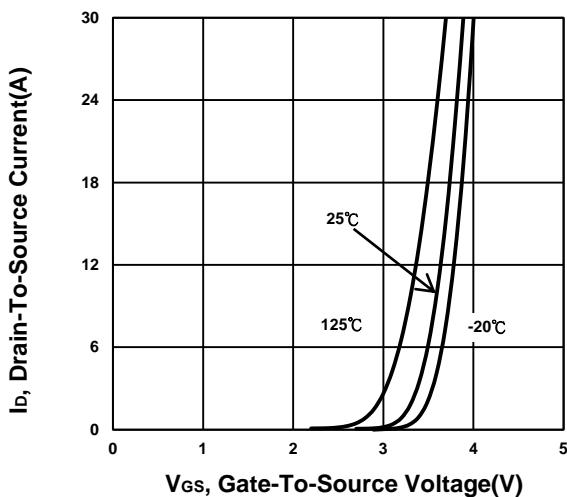
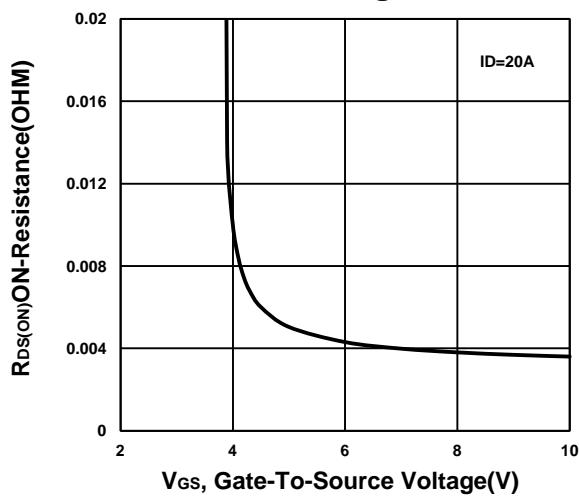
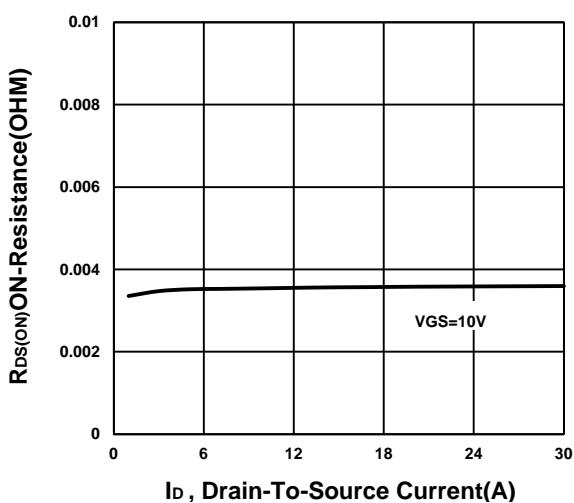
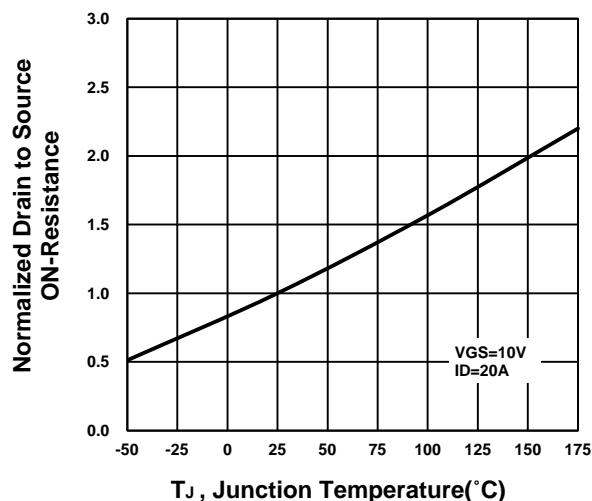
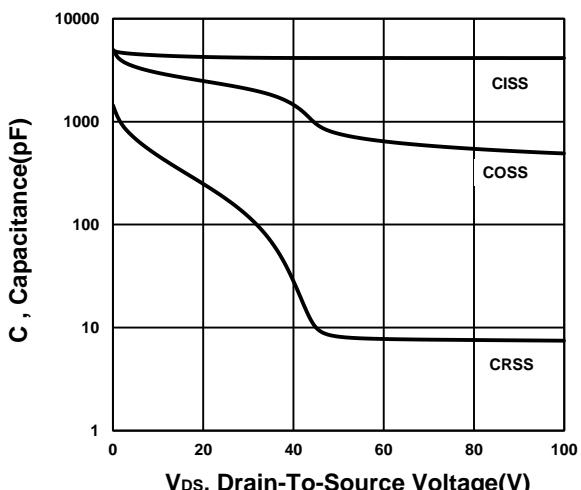
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|   |              |   |  |      |     |           |
|---|--------------|---|--|------|-----|-----------|
| Drain-Source On-State Resistance <sup>1</sup>   | $R_{DS(ON)}$ | $V_{GS} = 10V, I_D = 20A$   |  | 3.8  | 4.5 | $m\Omega$ |
| Forward Transconductance <sup>1</sup>   | $g_{fs}$     | $V_{DS} = 5V, I_D = 20A$  |  | 70   |     | S         |
| <b>DYNAMIC</b>  |              |   |  |      |     |           |
| Input Capacitance   | $C_{iss}$    | $V_{GS} = 0V, V_{DS} = 50V, f = 1MHz$                                 |  | 4145 |     | pF        |
| Output Capacitance  | $C_{oss}$    |   |  | 762  |     |           |
| Reverse Transfer Capacitance  | $C_{rss}$    |   |  | 10   |     |           |
| Gate Resistance   | $R_g$        | $V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$                                  |  | 1    |     | $\Omega$  |
| Total Gate Charge <sup>2</sup>  | $Q_g$        | $V_{GS} = 10V, V_{DS} = 50V, I_D = 20A$                               |  | 75   |     | nC        |
| Gate-Source Charge <sup>2</sup>   | $Q_{gs}$     |   |  | 17   |     |           |
| Gate-Drain Charge <sup>2</sup>  | $Q_{gd}$     |   |  | 23   |     |           |
| Turn-On Delay Time <sup>2</sup>   | $t_{d(on)}$  | $V_{DD} = 50V,$<br>$I_D \approx 20A, V_{GS} = 10V, R_{GEN} = 6\Omega$ |  | 22   |     | nS        |
| Rise Time <sup>2</sup>  | $t_r$        |   |  | 56   |     |           |
| Turn-Off Delay Time <sup>2</sup>  | $t_{d(off)}$ |   |  | 73   |     |           |
| Fall Time <sup>2</sup>  | $t_f$        |   |  | 79   |     |           |
| <b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b> |              |   |  |      |     |           |
| Continuous Current  | $I_S$        |   |  |      | 137 | A         |
| Forward Voltage <sup>1</sup>  | $V_{SD}$     | $I_F = 20A, V_{GS} = 0V$  |  |      | 1.2 | V         |
| Reverse Recovery Time   | $t_{rr}$     | $I_F = 20A, dI_F/dt = 100A/\mu s$                                     |  | 63   |     | nS        |
| Reverse Recovery Charge   | $Q_{rr}$     |   |  | 95   |     | nC        |

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.

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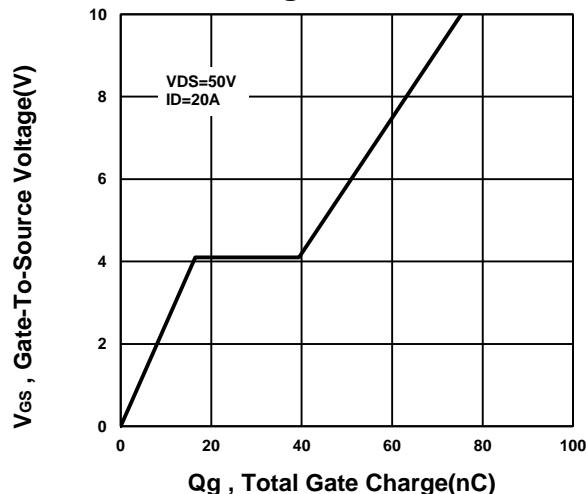
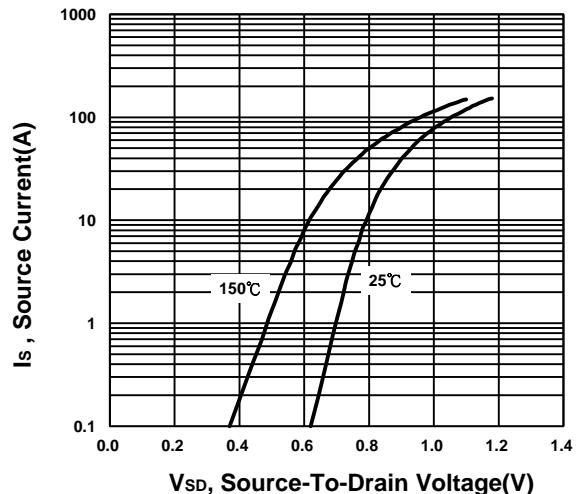
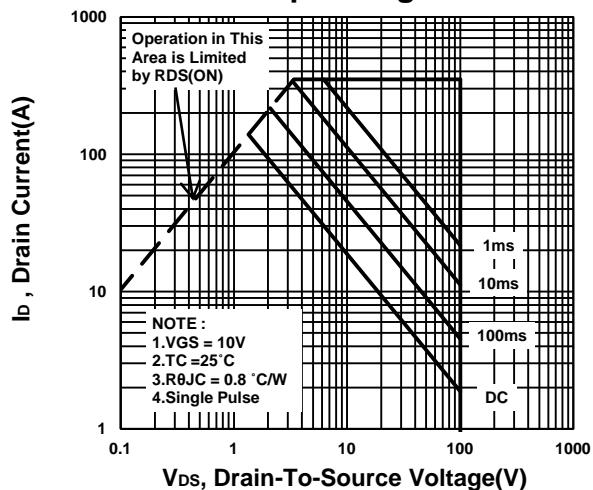
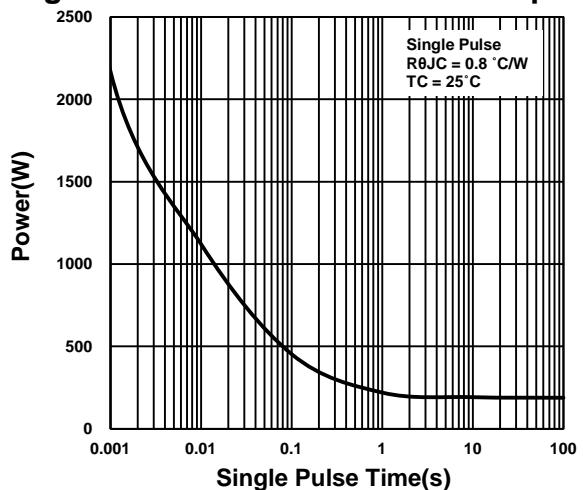
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**Output Characteristics****Transfer Characteristics****On-Resistance VS Gate-To-Source Voltage****On-Resistance VS Drain Current****On-Resistance VS Temperature****Capacitance Characteristic**

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**Gate charge Characteristics****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**